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V. 2  
10-28-03

Docket No.: 60188-124

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Yasuyuki DOI, et al.-

Serial No.: 10/019,437

Group Art Unit: 2821

Filed: December 31, 2001

Examiner: not yet assigned

For: LIQUID CRYSTAL DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE, REFERENCE VOLTAGE BUFFERING CIRCUIT, AND METHOD  
FOR CONTROLLING THE SAME (AS AMENDED)

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each non-English language reference was first cited in the attached International Preliminary Examination Report and its relevance discussed therein.

10/019,437

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**Date: May 7, 2003**